Amendments to the Specification:

Please replace the paragraph beginning on Page 6, line 1, with the following amended paragraph:

Fig. 3 shows the sequence of address translation to which an ATM packet is subjected when traveling from the core network to a terminal. A packet from the core network 2 has a VPI/$\underline{VCI}$ ~~VPI~~ part 31 as is shown in Fig. 3. At the interface P10 this VPI/$\underline{VCI}$ ~~VCI~~ part is translated into a VPI_C/VCI' part 35. This translation is performed by addressing a table 33 with the VPI/VCI part as input signal and reading the VPI_C/VCI' part from the output of the table 33. The table 33 is held by the translation means 6 in Fig. 2. As can be seen in Fig. 3 the complete address information VPI/VCI is used for addressing the table 33.

Please replace the paragraph beginning at Page 8, line 7, with the following amended paragraph:

The ~~VPI~~ $\underline{VCI_{PRIOR}}$ part of the address information 47 is used to route the ATM packet to the Network Control Node 12 via a path being able to provide transmission according to the proper Quality of Service indicated by the address part $\underline{VCI_{PRIOR}}$ ~~VCI'~~. The $\underline{VCI_{PRIOR}}$ ~~VCI'~~ part of the address information is used as input for the translation of the address information$\underline{.}$

Please replace the paragraph beginning at Page 8, line 11, with the following amended paragraph:

At interface P7, the $\underline{VCI_{PRIOR}}$ ~~VCI'~~ part of the address information 47 is used to address a table 49 from which the translated address information VPI_{OUT}/VCI' is read. The table 49 is held in the translation means 10 in Fig. 4. The part VPI_{OUT}

indicates the output of the cross connect 8 to which the packet

should be transferred.